

PATENT APPLICATION Docket No. 9898-293 Client No. SS-17762-US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Enclosed for filing is a patent application under 37 CFR 1.53(b) of:

Inventor:

Hoe-Ju CHUNG and Kyu-Hyoun KIM

For:

SEMICONDUCTOR MEMORY DEVICE HAVING DUTY CYCLE

CORRECTION CIRCUIT AND INTERPOLATION CIRCUIT

INTERPOLATING CLOCK SIGNAL IN THE SEMICONDUCTOR

MEMORY DEVICE

Applicant requests FIG. 5 to be published with the application.

Enclosures:

Specification (pages 1-11); claims (pages 12-16); abstract (page 17)
SIX sheet(s) of FORMAL drawings (comprising 10 figures)

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CLAIMS AS FILED						
For	Number Filed	Number Extra	Rate		Basic Fee \$750	
Total Claims	15-20	0	x \$ 18	=	\$0.00	
Independent Claims	4-3	1	x \$ 84	=	\$84.00	
TOTAL FILING FEE		·			\$834.00	

\bowtie	Certified Korean Priority Document	No. 2002-53327, filed September 4, 2002			
\boxtimes	Combined Declaration and Power of Attorney				
\boxtimes	Newly executed (original or copy)				
	Assignment with cover sheet				
	Assignee Name and Address:	Samsung Electrinics Co., Ltd.			
		Suwon-city, Kyungki-do Republic of Korea			
		ard payment of \$834.00, filing fee (\$750.00) and			
	additional claims fee (\$84.00) is enc	losed.			
\boxtimes	Any deficiency or overpayment should be charged or credited to deposit account				
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Customer No. 20575

Respectfully submitted,

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